

AMENDMENTS TO THE CLAIMS:

Please amend the claims as follows:

Claims 1-11 (Canceled)

12. (New) An image sensor in which a gate of a source follower transistor is connected to one end of a photoelectric transducer, and a potential at the one end of the photoelectric transducer is read out as image data through the source follower transistor, the image sensor characterized in that

Q / at a time of reset of the photoelectric transducer, a differential amplifier is constructed in which an inverting input terminal is the gate of the source follower transistor, and a noninverting input terminal is a gate of a first differential transistor to which a constant voltage is applied, and the one end of the photoelectric transducer is kept the constant voltage by feeding back an output of the differential amplifier to the inverting input terminal.

13. (New) An image sensor according to claim 12, characterized in that the output of the differential amplifier is fed back to the inverting input terminal through a reset transistor for resetting the photoelectric transducer.

14. (New) An image sensor according to claim 13, characterized in that the photoelectric transducer, the source follower transistor, and the reset transistor constitute a pixel, and a plurality of such pixels are arranged in a matrix form.

15. (New) An image sensor according to claim 14, characterized in that outputs of the plurality of the pixels are connected in common to one vertical selection line, and the image data of the respective pixels are read out through the vertical selection line.

16. (New) An image sensor according to claim 15, characterized in that the first differential transistor constituting the differential amplifier is provided for each of the vertical selection lines.

17. (New) An image sensor according to claim 16, characterized in that the first differential transistor is connected to the vertical selection line through a circuit switching transistor which is turned on in synchronization with an on operation of the reset transistor.

Q! 18. (New) An image sensor according to claim 17, characterized in that reset transistors of the plurality of the pixels are connected in common to one reset voltage supply line.

19. (New) An image sensor according to claim 18, characterized in that the differential amplifier includes a current mirror circuit, and the current mirror circuit is provided for each of a plurality of such reset voltage supply lines, and supplies the output of the differential amplifier to each of the plurality of the reset transistors connected in common to the reset voltage supply line.

20. (New) An image sensor according to claim 19, characterized in that the vertical selection line and the reset voltage supply line form a pair and are disposed in parallel with each other.
